Amendments To The Claims

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

Listing of claims:

1.   (**currently amended**) An XML processor comprising:

receiving an XML document:

a first memory storing software for performing an XML processing, variables, and values required to execute software **on the received XML document**;

a hardware processing module performing a part of the XML processing in a hardware manner **on the received XML document, and**

wherein the hardware processing module is separate and independent of the first memory storing having the software for performing the XML processing;

a second memory employed by the hardware processing module; and

a CPU controlling the XML processing **on the received XML document** by the software stored in the first memory **to generate a first output if the XML is executed by software, and to generate a second output if the part of the XML processing is performed in the hardware manner,**

wherein the XML processing time is reduced from the hardware processing module performing the part of the XML processing in the hardware manner, and

wherein the first and second outputs are equivalent.

2.   (**currently amended**) The XML processor according to claim 1, wherein the hardware processing module performs a memory management function used in XML

parsing, ~~i.e.,~~ **from at least one of** assignment, return, and reassignment of memory among XML processing functions.

3.   (**currently amended**) The XML processor according to claim 2, wherein the hardware processing module processes **the** assignment, **the** reassignment, and **the** return of memory with respect to XML elements which are expressed as nodes and a tree relation between the nodes, the module comprising:

a node usage check table divided into several blocks, each block indicating whether to use a corresponding node table;

a node table managing the whole information that each node has to store, ~~i.e.,~~ **at least one of** a node name, a node type, a parent node, a child node~~, and the like~~; and

a node memory storing actual content of every component of the node table.

4.   (original) The XML processor according to claim 3, wherein the node table has addresses in which every component on the node memory is respectively stored.

5.   (original) The XML processor according to claim 1, wherein the hardware processing module performs an XML DTD processing.

6.   (original) The XML processor according to claim 1, wherein the hardware processing module performs a state machine of an XML schema.

7 – 9.   (**canceled**)